

IN THE CLAIMS:

Please cancel Claim 4, and amend Claims 1 and 5, as shown below.

1. (Currently Amended) A gate driving circuit comprising:
 - a direct current power source;
 - a driving signal source for outputting a high-level or low-level signal;
 - a main switch device, having a gate terminal in which the signal outputted from the driving signal source is inputted, for controlling a conduction state between a source terminal and a drain terminal in correspondence with a level of the signal;
 - a load energized when the conduction state between the source terminal and the drain terminal becomes ~~a conductive state~~ conductive;
 - reverse current blocking means, connected between the driving signal source and the gate terminal, for outputting a signal only in a direction from the driving signal source to the gate terminal; and
 - regenerative means for regenerating a part of power outputted by the driving signal source in the direct current power source, the regenerative means being connected between the gate terminal and a high potential side of the direct current power source, which becomes a conductive state and the regenerative means becoming conductive when the conduction state between the source terminal and the drain terminal is ~~a non-conductive state~~ non-conductive,
 - wherein a gate-source threshold voltage to ~~obtain the conductive state~~ make the conduction state between the source terminal and the drain terminal conductive is higher than an output voltage of the direct current power source, and

wherein the regenerative means includes a MOSFET.

2. (Original) The gate driving circuit according to claim 1, wherein the main switch device includes an N-channel MOSFET or an N-channel IGBT.

3. (Original) The gate driving circuit according to claim 1, wherein the reverse current blocking means includes a diode.

4. (Cancelled)

5. (Currently Amended) A power source circuit comprising:
a gate driving circuit having: a direct current power source; a driving signal source for outputting a high-level or low-level signal; a main switch device, having a gate terminal in which the signal outputted from the driving signal source is inputted, for controlling a conduction state between a source terminal and a drain terminal in correspondence with a level of the signal; a load energized when the conduction state between the source terminal and the drain terminal becomes ~~a conductive state~~ conductive; reverse current blocking means, connected between the driving signal source and the gate terminal, for outputting a signal only in a direction from the driving signal source to the gate terminal; and regenerative means for regenerating a part of power outputted by the driving signal source in the direct current power source, the regenerative means being connected between the gate terminal and a high potential side of the direct current power source, which becomes a and the regenerative means becoming ~~conductive state~~ when the

conduction state between the source terminal and the drain terminal is a ~~non-conductive state~~ non-conductive,

wherein in the gate driving circuit, a gate-source threshold voltage to ~~obtain the conductive state~~ make the conduction state between the source terminal and the drain terminal conductive is higher than an output voltage of the direct current power source,

~~and~~ wherein when the conduction state between the source terminal and the drain terminal becomes ~~the conductive state~~ conductive, the output voltage from the direct current power source is supplied to the load, and

wherein the regenerative means includes a MOSFET.

6. (Original) The power source circuit according to claim 5, wherein the load includes a primary-side coil of a transformer.

7. (Previously Presented) The power source circuit according to claim 5, wherein the power source circuit performs DC/AC conversion.

8. (Original) The power source circuit according to claim 5, wherein the direct current power source includes plural solar cells which are not serially connected.